422 - Hardware & Organization

Progress Report #3

**Smart Cafe**

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## What We Accomplished:

* I/O routines written; other APIs documented in code
* Writing the entire report to a text file.
* Green highlights denote completed and tested and red denotes still not completed

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| **Instructions:** | | **Addressing Modes:** |
| 1. NOP 2. RTS 3. JSR 4. LEA 5. NOT 6. DIVU 7. MULS 8. ADD 9. SUB 10. AND | 1. MOVE 2. LSL 3. LSR 4. ASL 5. ASR 6. MOVEM 7. BLT 8. BGE 9. BEQ 10. BRA | 1. Data Register Direct 2. Address Register Direct 3. Address Register Indirect 4. Immediate Addressing 5. Address Register Indirect with Post Incrementing 6. Address Register Indirect with Pre Decrementing 7. Absolute Long Address 8. Absolute Word Address |

## Problems Encountered:

* We had a little issue with miscommunication with who needs to do what. We met up and cleared any confusion.

## Are We on Track?

* Yes, we just have a few opcodes left and integrating EA modes into opcodes, but other than that, our project looks like it's in a very good shape.

## Next Progress Reports:

1. Error handling
2. Remaining instructions (MOVEM, BLT, BGE, BEQ, BRA)
3. Check against Test
4. Write our own test file
5. Integrate EA Modes into Opcodes
6. Gather final report